

ROBERT CHARLES BURK

Nevada City, CA 95959

530-263-7978
robert.burk@ieee.org

SUMMARY

An analytical, client-focused, and flexible Test Engineering and Operations leader with extensive experience in electronics test and manufacturing. Highly skilled in the analysis and creation of test solutions for complex electronic systems, process development, and manufacturing quality improvement. Also skilled in implementation of Lean Manufacturing, selection and development of contract manufacturers, and coordination of international development teams. Familiar with:

SAP	C/C++	MS Office Suite	MS Project
QAD	Serial Digital Video	MS Access	MS Visio
Visual Basic	IEEE-1149 (JTAG)	Windows NT/Vista	DOS
HTML	ISO 9001:2000	Visual SourceSafe	TCP/IP

Also proficient in the use of most electronic test equipment and ATE systems. Seeking to continue a career in engineering leadership, particularly involving test, project management, and quality assurance.

PROFESIONAL ACCOMPLISHMENTS

- Managed a staff of 18 Test Engineers and a budget of over \$2.5M. Controlled spending to stay within budget every year.
- Headed-up an international team of software developers to develop system diagnostics. Using off-shore developers reduced the cost by more than 50%. Diagnostics reduced system test time from 40 hours to 10 hours and improved system test yield.
- Created Boundary Scan test of a new board design; achieved over 98% test coverage.
- Engineered C code to manage a new test template database and translate a customer's existing template database; resulting programs were robust and without known bugs.
- Outsourced boards test to contract manufacturer; resulted in cost savings of over \$3M annually.
- Transferred manufacturing and test of a new system design from a design center in Germany to the U.S.; reduced the build cost of the system by about 10%, lead to the closing of the production facility in Germany, further reducing expenses.
- Evaluated test capabilities of domestic and off-shore contract manufacturers; on-going cost savings ensured our ability to stay competitive and decreased cost by about 8%.
- Trouble shot board failures, analyzed failure trends, and performed root cause analysis; implemented corrective actions, and reduced the discrepant material inventory by nearly 50%. Also provided root cause data for customers.
- Remodeled the test area to implement Lean Manufacturing; resulted in reducing delivery lead times by 5 days and smoothing the end of period production spikes.

- Created new work processes and standard operating procedures in support of ISO 9001 certification; achieved ISO 9001 certification on first audit.
- Developed test plans and gained approval for the test of new products; delivered test solutions on-time and prior to first customer ship.
- Audited test times for over 800 line items annually; re-set the standard test time as appropriate. Maintained test standards in SAP for more accurate resource planning and setting of price margins.

EMPLOYMENT HISTORY

THOMSON GRASS VALLEY , Nevada City, CA	1998 - 2009
<i>Lead Test Engineer</i>	2004 - 2009
<i>Test Engineering Manager</i>	2001 - 2004
<i>Embedded Test Engineer</i>	1998 - 2001
TERADYNE, INC. , Walnut Creek, CA	1987 - 1998
<i>Test Engineering Lead</i>	1996 - 1998
<i>Software Development Engineer</i>	1994 - 1996
<i>Test Engineering Technologist</i>	1987 - 1994

EDUCATION

- UNIVERSITY OF THE PACIFIC**, Stockton, CA
B.S., Electrical Engineering
- SIERRA COLLEGE**, Rocklin, CA
Completed courses in C++ Programming and Data Communications
- DIABLO VALLEY COLLEGE**, Pleasant Hill, CA
Completed a course in C Programming

TRAINING

(Employer sponsored courses)

- | | |
|-----------------------------------|-------------------------------|
| 9-Step Structured Problem Solving | Asset ScanWorks Development |
| Bourne Shell Programming | Economics of Test and DFT |
| Embedded System Test Methods | Employment Interviewing |
| Exercising Influence | FlowVision Lean Manufacturing |
| Introduction to Broadcast Video | Managing Teams |
| MS Access - Intermediate | MS Excel - Advanced |
| Situational Leadership | SMT Design for Manufacturing |
| Supervisor Training | Teradyne Victory Training |
| Test, ATE and Testability | TQM Concepts |